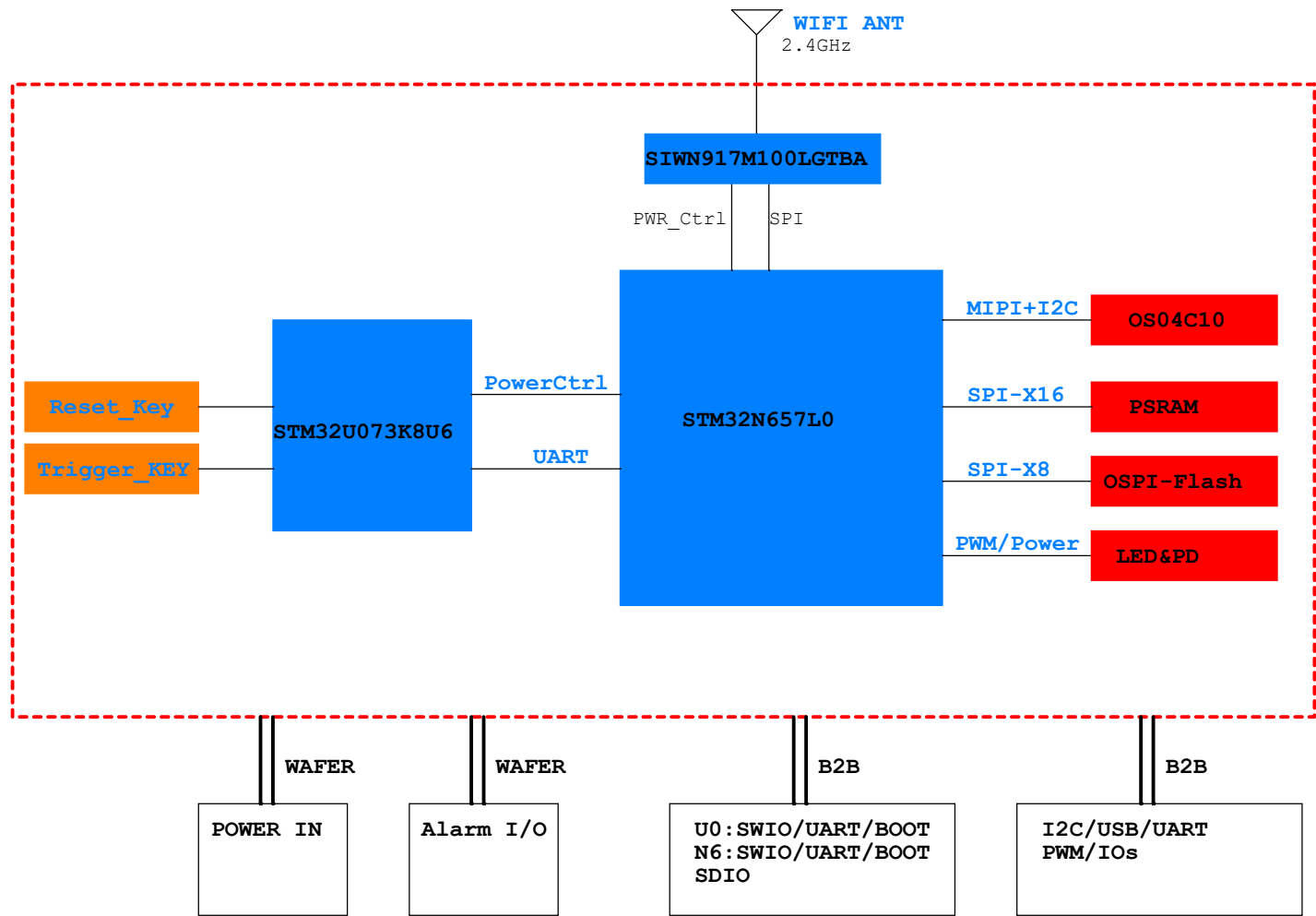
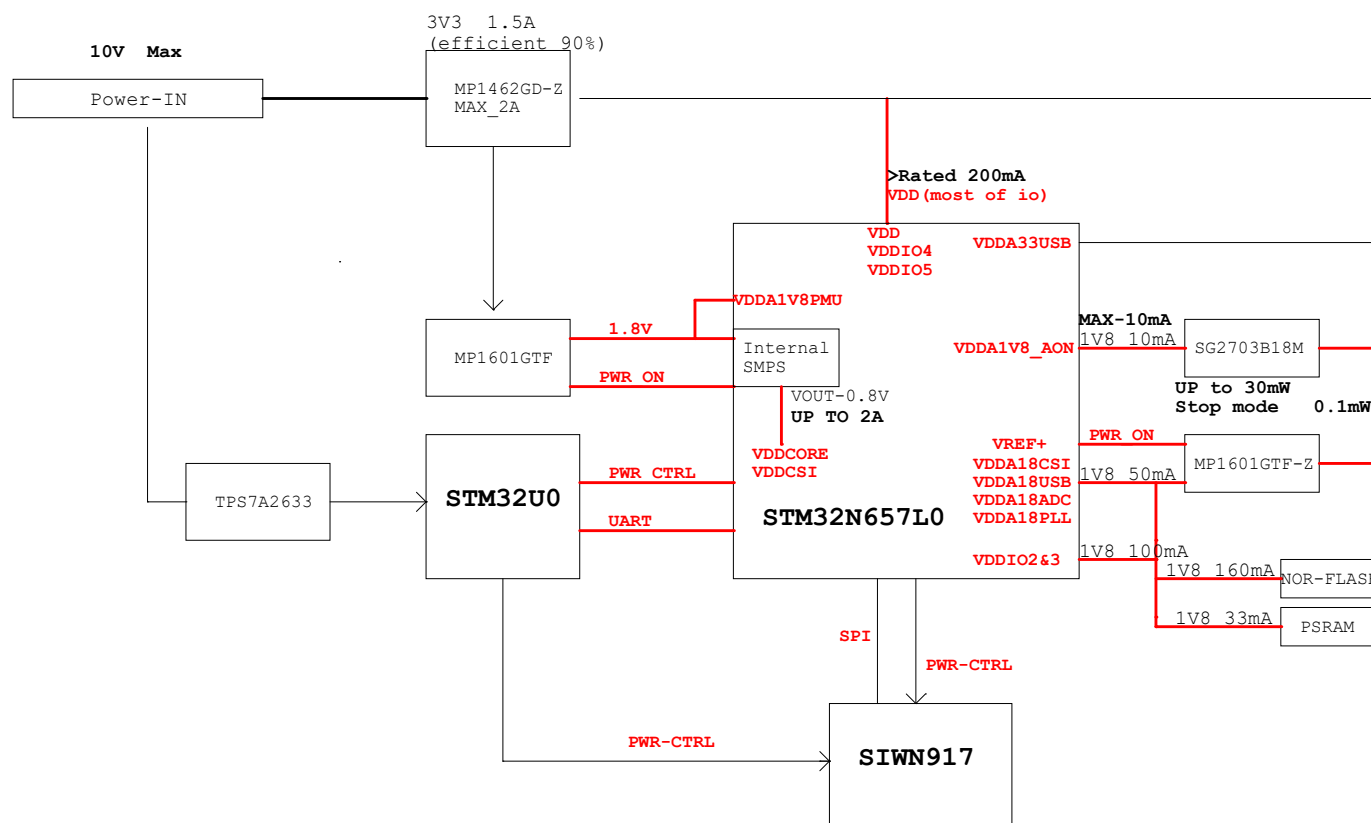


BLOCK DIAGRAM

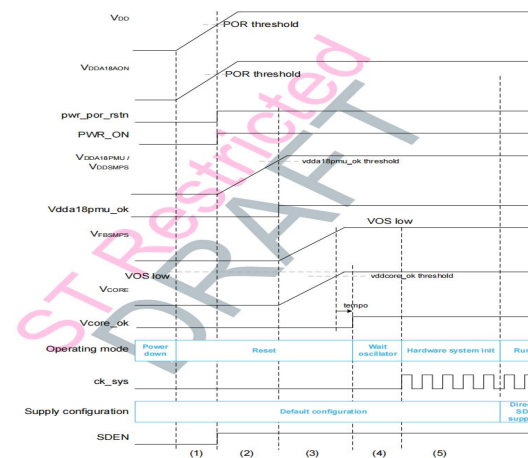


SPI/USB/Power
Cat-1 Module

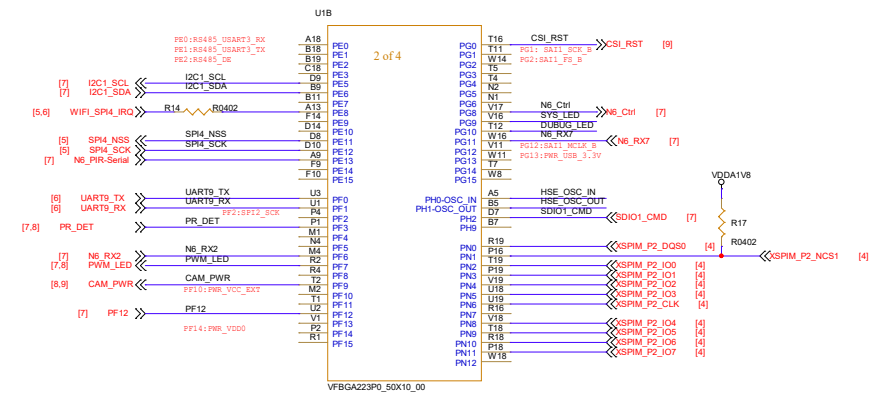
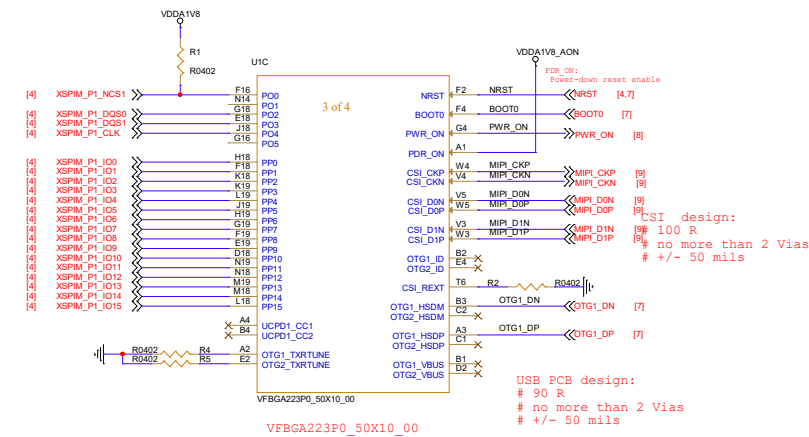
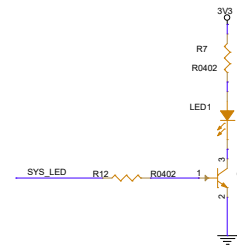
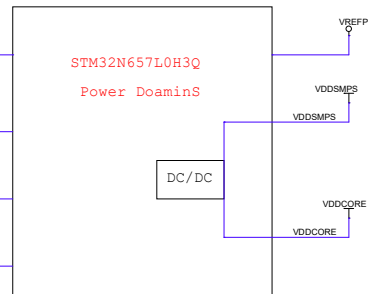
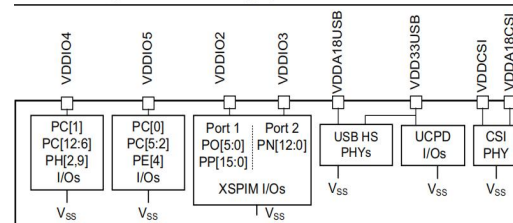
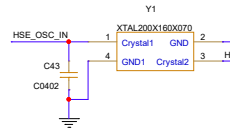
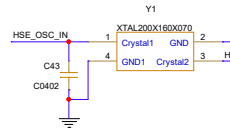
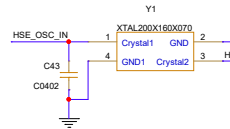
POWER TREE



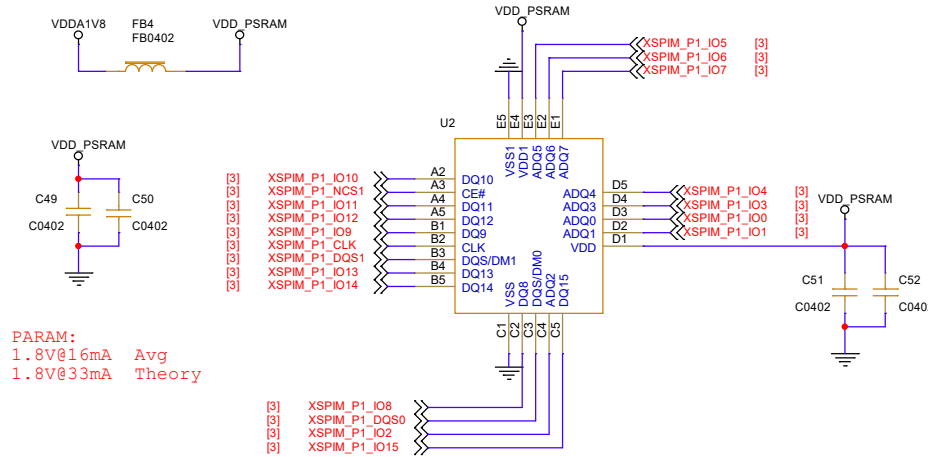
MCU上电时序



STM32N657L0H3Q



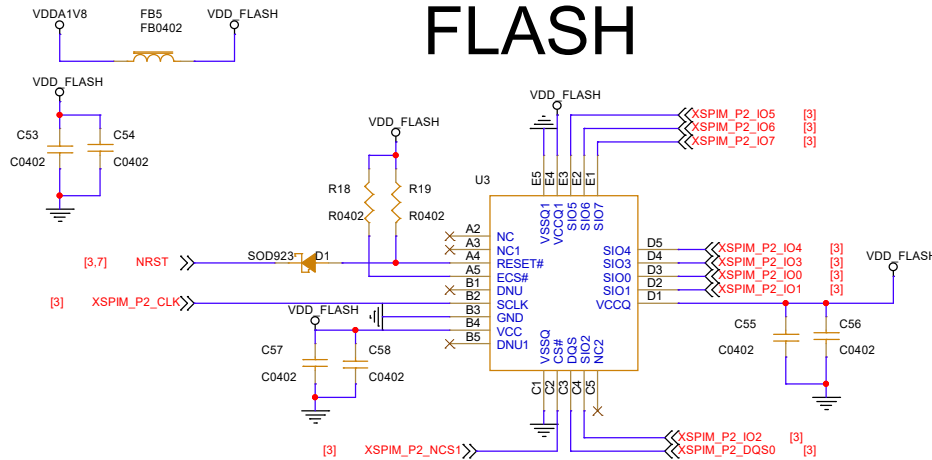
PSRAM



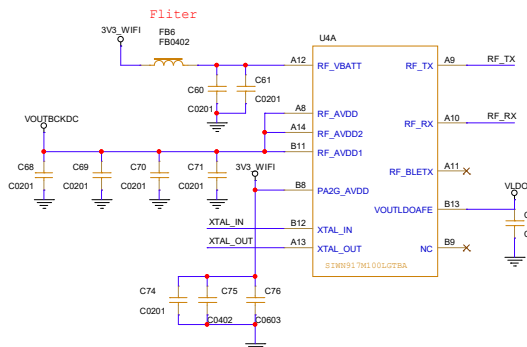
PARAM:
1.8V@16mA Avg
1.8V@33mA Theory

Flash:
1.8V@32mA Max
1.8V@13mA Avg
1.8V@100mA Theory

FLASH

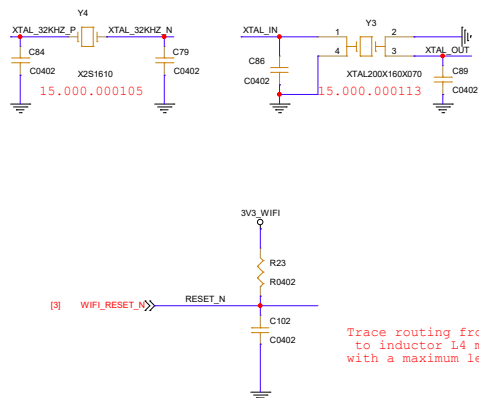
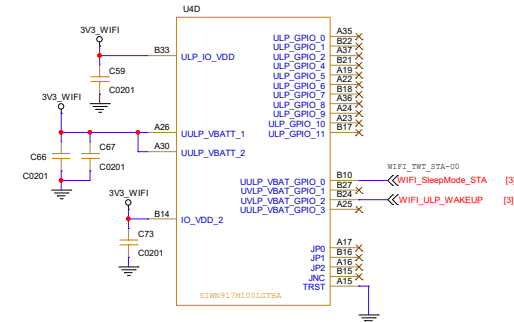


Alt for MX66UM1G45G:
1Gbit: DS-00683-GD55LX01GE
512Mbit: DS-00596-GD25LX512ME --Default
256Mbit: DS-00514-GD25LX256E

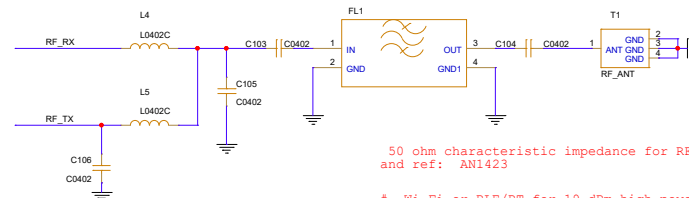
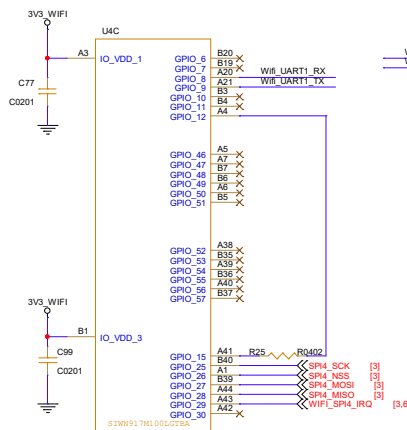
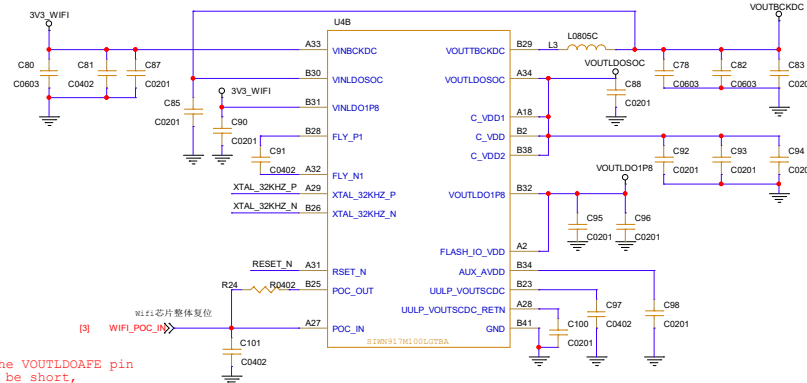


Use star routing for PA2G_AVDD and RF_VBATT

Recomment all Power Pin Star Routed



Trace routing from the VOUTLDOAFE pin to inductor L4 must be short, with a maximum length of 15 mm



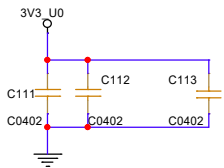
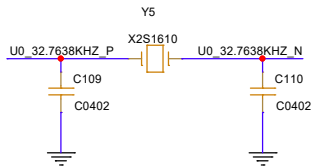
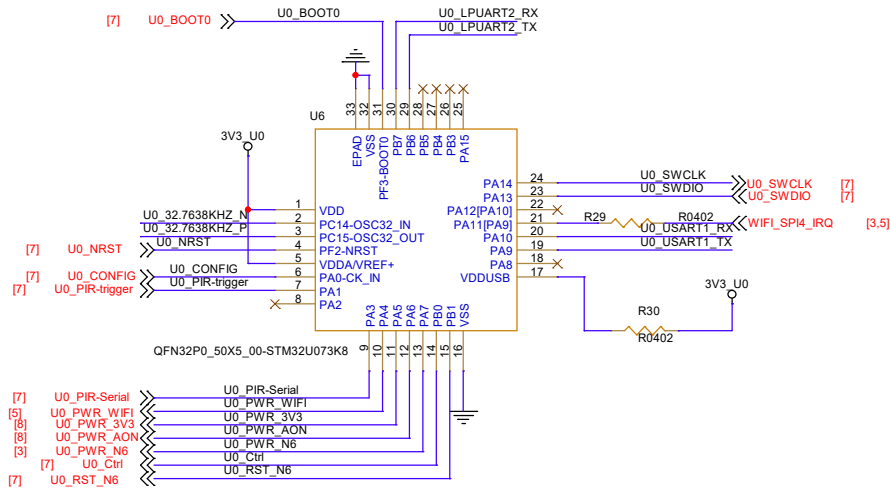
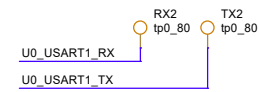
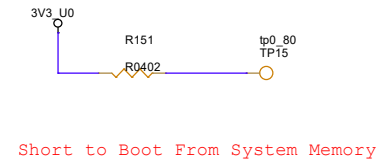
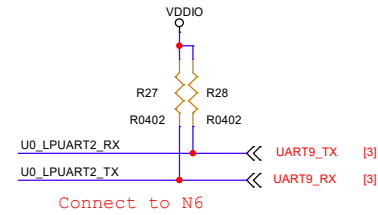
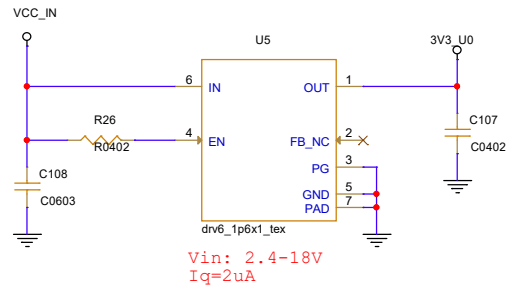
50 ohm characteristic impedance for RF traces
and ref: AN1423

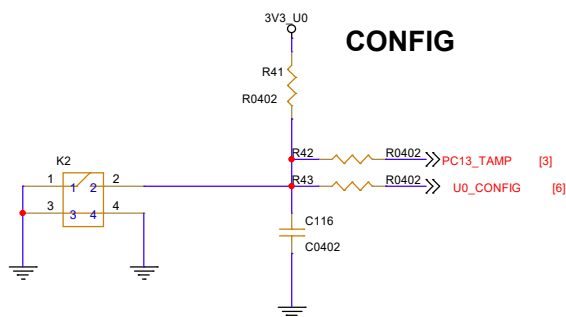
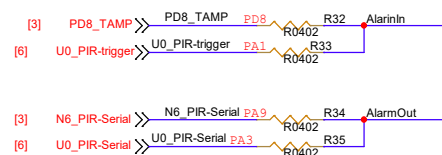
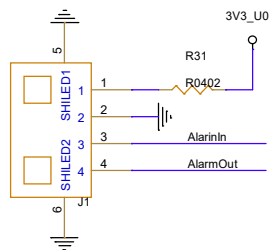
```
# Wi-Fi or BLE/BT for 19 dBm high power: RF_TX and RF_RX serving as the TX and RX ports respectively.
# BLE/BT for 0 dBm low power: RF_RX serving as both the TX and RX ports.
# BLE/BT for 8 dBm high power: RF_BT_TX and RF_RX serving as the TX and RX ports respectively.
```

Table 4. SKY13317-373LF Truth Table

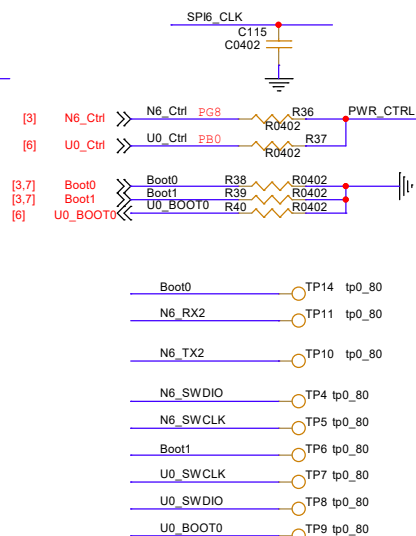
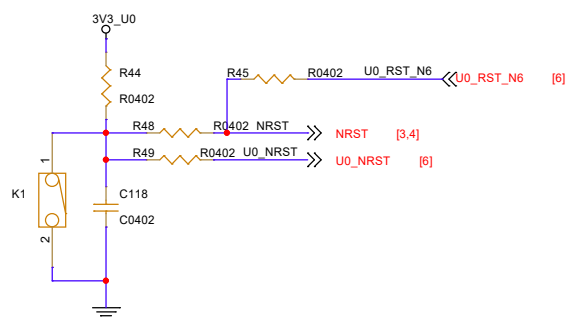
Low Insertion Loss Path	V1 (Pin 3)	V2 (Pin 6)	V3 (Pin 7)
RFC to RF1	High	Low	Low
RFC to RF2	Low	High	Low
RFC to RF3	Low	Low	High

Note: "High" = 1.8 to 5.0 V. "Low" = 0 to 0.25 V. Any state other than described in this table places the switch into an undefined state. An undefined state will not damage the device.



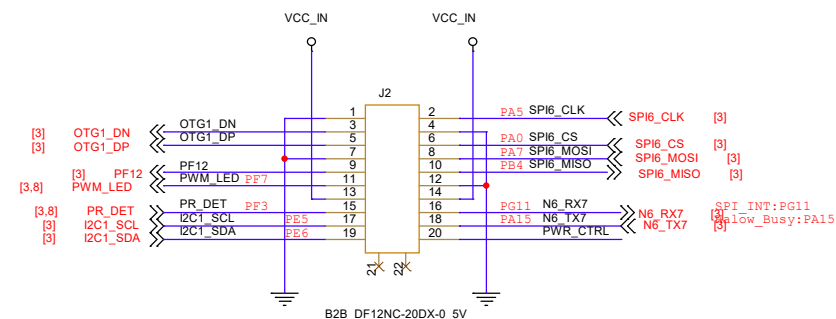
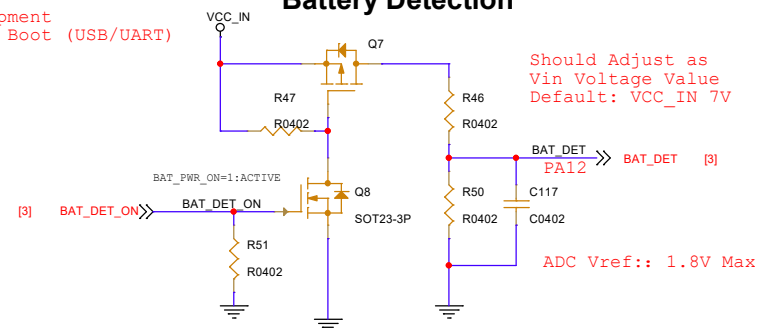


SYS Reset

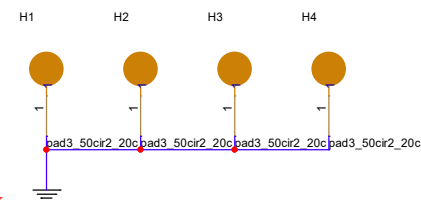
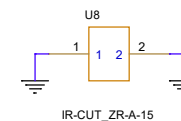
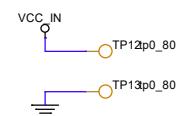
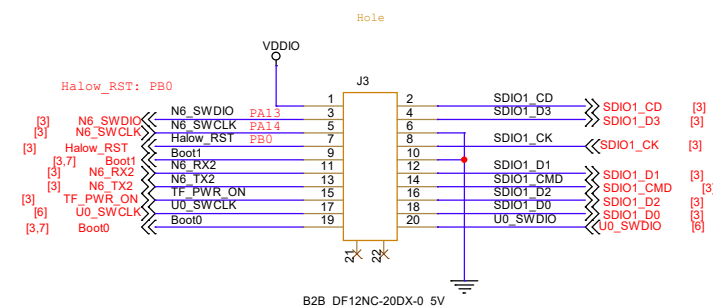


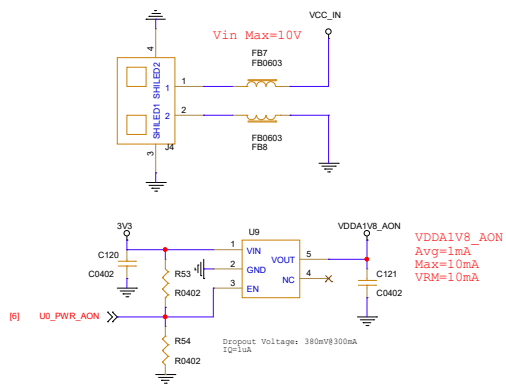
```
N6 Boot[0:1]
00: Flash
x1: Development
10: Serial Boot (USB/UART)
```

Battery Detection



SPI1/2/3/6 Support I2S





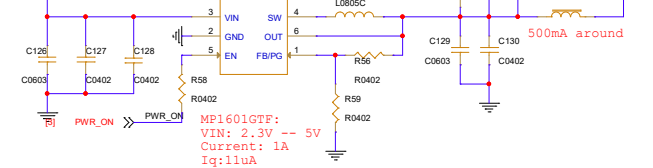
3V3-->1V8

Input: 3.3V@300mA around

MWTC252010S2R2MT

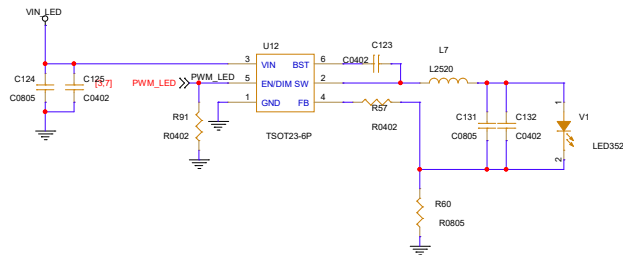
change to:

MWTC201210S2R2MT 0.1R 1.9A

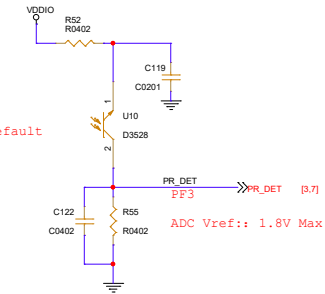


VDDA1V8:
PSRAM: 33mA
Flash: 100mA
VDDIO2 1V8: PSRAM ?mA
VDDIO3 1V8: FLASH ?mA
VDDA1V8 as below: 15mA
ADC 1V8
CSI 1V8
PLL 1V8
USB 1V8

VDD1V8 for:
VDDSMPS 250mA
VDDA1V8MPU



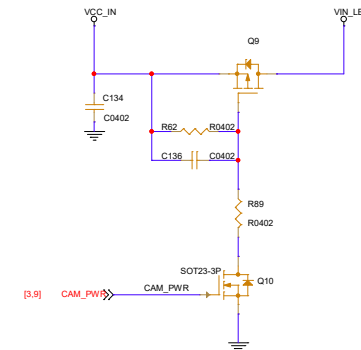
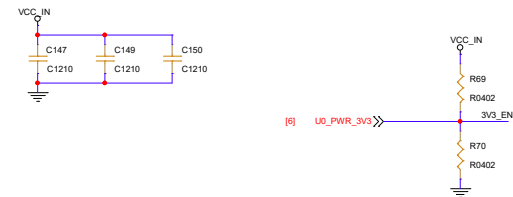
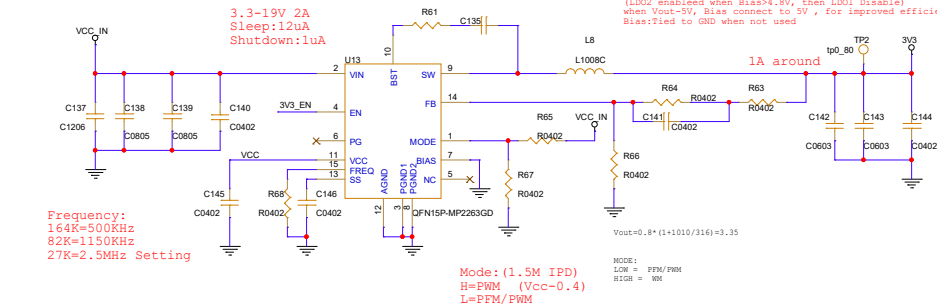
LXD/GB5-A1CV ISS:
120uA@200Lux 3.3V-Rt=27.5K --Default
-->Rd=27.5K*1.8/3.3=15K
60uA@100Lux 3.3V-Rt=55K
-->Rd=55K*1.8/3.3=30K
6uA@10Lux: Vo=6uA*30k=180mV



VIN-->3V3

IC Powered:
LD01: By Vin, >5V, Full Regulation
LD02: By Bias, should up to 4.8V, then VCC internal circuit are powered by BIAS
(LD02 enabled when Bias>4.8V, then LD01 Disable)
When Vout<5V, Bias connect to 3V, for improved efficiency
Bias:Tied to GND when not used

3V3 for:
N6-VDDIO: 10mA
N6-1.8 DC-DC 300mA
N6-1.8 AON
OS04C10: 60mA
SIWN917: 300mA



A vertical number line with four points labeled A, B, C, and D from bottom to top. An arrow points from point B to point C.

